BULETINUL INSTITUTULUI POLITEHNIC DIN IAŞI
TOMUL LIV (LVIII), FASC. 3, 2008
ELECTROTEHNICĂ, ENERGETICĂ, ELECTRONICĂ

ADVANCED SIGNAL GENERATOR ARHITECTURE: ANALOG OUTPUT TO ADVENCED FEATURES

BY

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Abstract. Signal generators use the synchronization and memory core (SMC) architecture to provide a common interface between onboard memory, external hardware, and the digital-to-analog converter (DAC) of the device. In this white paper we will differentiate between two types of signal generators - arbitrary function generators and arbitrary waveform generators. In addition, we will discuss various aspects of signal generators including the memory architecture, DAC considerations, digital gain, filtering and interpolation, and various events.

Keywords: Interpolation, digital gain