BULETINUL INSTITUTULUI POLITEHNIC IAȘI TOMUL LIV (LVIII), FASC. 4, 2008 ELECTROTEHNICĂ, ENERGETICĂ, ELECTRONICĂ

USE OF PROGRAMMABLE LOGIC CIRCUITS FOR THE CONTROL OF ELECTRICAL MACHINES

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Abstract. In this paper we present our work within the PSICOME project which aims to develop a methodology for the design of electrical machines control based on field programmable gate arrays (FPGAs) and complex programmable logic devices (CPLDs).

Keywords: electrical machine control, programmable logic circuits, FPGA, CPLD